

**Elektroniska komponenter/Electronic Devices**

*Journals and International Conferences*

S. Azam, C. Svensson, and Q. Wahab, "Pulse Input Class-C Power Amplifier Response of SiC MESFET using Physical Transistor Structure in TCAD", in *J. Solid-State Electronics*, vol 52, page 740-744, May 2008.

A. Kashif, T. Arnborg, T. Johansson, C. Svensson, and Q. Wahab, "Influence of interface state charges on RF performance of LDMOS transistor", in *J. Solid-State Electronics*, Vol 52, pp. 1099-1105, May 2008.

M. Anders, H. Kaul, M. Hansson, R. Krishnamurthy, and S. Borkar, "A 2.9Tb/s 8W 64-Core Circuit-switched Network-on-Chip in 45nm CMOS", in *proc. 34<sup>th</sup> European Solid-State Circuits Conference (ESSCIRC'08)*, pp. 182-185, Edinburgh, UK, 2008.

H. Fredriksson and C. Svensson, "2.6 Gb/s Over a Four-Drop Bus Using an Adaptive 12-Tap DFE", in *proc. 34<sup>th</sup> European Solid-State Circuits Conference (ESSCIRC'08)*, Edinburgh, UK, pp. 470-473, 2008.

B. Mesgarzadeh and A. Alvandpour, "A 2-GHz 7-mW Digital DLL-Based Frequency Multiplier in 90-nm CMOS", in *proc. 34<sup>th</sup> European Solid-State Circuits Conference*, Edinburgh, UK, pp. 86-89, 2008.

R. Ramzan and J. Dabrowski, "On-chip Calibration of RF Detectors by DC Stimuli", in *proc. IEEE Radio Frequency Integrated Circuits Symposium (RFIC'08) Atlanta Georgia, USA, June 15-17, 2008*.

J. Fritzin, T. Johansson, and A. Alvandpour, "A 72.2Mbit/s LC-based Power Amplifier in 65nm CMOS for 2.4GHZ 802.11N WLAN", in *proc. 15<sup>th</sup> Int. Conference Mixed Design of Integrated Circuit and Systems (MIXDES'08)*, pp. 155-158, Poznan, Poland, 2008.

S. Ahmad and J. Dabrowski, "ADC On-Chip Dynamic Test by PWM Technique", in *proc. International Conference on Signals and Electronic Systems*, pp. 15-18, Krakow, Poland, 2008.

N. Ahsan, C. Svensson, and J. Dabrowski, "Highly Linear Wideband Low Power Current Mode LNA", in *proceedings of International Conference on Signals and Electronic Systems*, pp. 73-76, Krakow, Poland, 2008.

N. Ahsan, J. Dabrowski, and A. Ouacha, "A Self-Tuning Technique for Optimization of Dual Band LNA", in *proc. 38<sup>th</sup> IEEE European Microwave Conference (EuMC'08)*, pp. 178-181, Amsterdam, Netherlands, 2008.

J. Fritzin, T. Johansson, and A. Alvandpour, "Impedance Matching Techniques in 65nm CMOS Power Amplifiers for 2.4GHz 802.11n WLAN", in *proc. 38<sup>th</sup> IEEE European Microwave Conference (EuMC'08)*, pp. 1207-1210, Amsterdam, Netherlands, 2008.

J. Fritzin and A. Alvandpour, "A 72.2Mbit/s Transformer-Based Power Amplifier in 65nm CMOS for 2.4GHz 802.11n WLAN" in proc. IEEE NORCHIP Conference, pp. 53-56, Tallin, Estonia, 2008.

A. Kashif, S. Azam, C. Svensson, and Q. Wahab, "Flexible power amplifier designing from device to circuit level by computation load-pull simulation technique", in proc. 23<sup>rd</sup> Symposium on Microelectronics Technology and Devices, pp. 233-239, Gramado, Brazil, 2008.

G. Mehdi, N. Ahsan, A. Altaf, and A. Eghbali, "A 403-MHz Fully Differential Class-E Amplifier in 0.35 $\mu$ m CMOS for ISM Band Applications", in proc. IEEE East-West Design & Test Symposium (EWDTS'08), pp. 239-242, Lviv, Ukraine, 2008.

T. Sundström, B. Mesgarzadeh, M. Krysender, M. Klein, I. Söderquist, A. Crona, T. Fransson, and A. Alvandpour, "Prognostics of Electronic Systems through Power Supply Current Trends", in proc. IEEE International Prognostics and Health Management Conference (PHM'08), Denver, USA, 2008.

T. Sundström and A. Alvandpour "A 2.5-GS/s 30-mW 4-bit Flash ADC in 90nm CMOS", in proc. 26th Norchip conference, pp. 264-267, Tallin, Estonia, 2008.

Christer Svensson, "The Challenge of an RF Frontend for Software Defined Radio", invited paper, Software Defined Radio Europe, London, UK, 2008.

### *National Conferences*

H. Fredriksson and C. Svensson, "Improvement potential of multi-drop DRAM memory buses", in proc. Swedish System-on Chip Conference (SSoCC'08), Gnesta, 2008.

J. Fritzin, T. Johansson, and A. Alvandpour, "Power Amplifiers for WLAN in 65nm CMOS", in proc. Swedish System-on Chip Conference (SSoCC'08), Gnesta, 2008.

B. Mesgarzadeh and A. Alvandpour, "A 2.5-GHz DLL-Based Multiphase Clock Generator in 90-nm CMOS", in proc. Swedish System-on Chip Conference (SSoCC'08), Gnesta, 2008.

R. Ramzan and J. Dabrowski, "Calibration of On-chip RF Detectors by DC Stimuli and Artificial Neural Networks", in proc. Swedish System-on Chip Conference (SSoCC'08), Gnesta, 2008.

T. Sundström and A. Alvandpour, "Taking Advantage of Process Variations in a Reference-free Flash ADC", in proc. Swedish System-on Chip Conference (SSoCC'08), Gnesta, 2008.

### *Dissertations – Compilations*

H. Fredriksson, "Improvement Potential and Equalization Circuit Solutions for Multi-drop DRAM Memory Buses", Dissertation No. 1177, ISBN: 978-91-7393-910-2, Linköping, June 2008.

M. Hansson, "Low-Power Clocking and Circuit Techniques for Leakage and Process Variation Compensation", Dissertation No. 1197, ISBN: 978-91-7393-847-1, Linköping, August 2008.

B. Mesgarzadeh, "Low-Power Low-Jitter Clock Generation and Distribution", Dissertation No. 1208, ISBN: 978-91-7393-817-4, Linköping, October 2008.

*Licentiate Thesis*

N. Ahsan, "Programmable and Tunable Circuits for Flexible RF Front Ends", Thesis No. 1379, ISBN: 978-91-7393-815-0, Linköping, 2008.

R. Ramzan, "Flexible Wireless Receivers: On-Chip Testing Techniques and Design for Test", Thesis No. 1378, ISBN: 978-91-7393-816-7, Linköping, 2008.